

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): **Shaw (et al.)**  
Serial No.: **09/051,263**  
Filed: **August 7, 1998**

Attorney Docket: **0081-012**  
Examiner: **Aimee J. Li**  
Group Art Unit: **2183**

Title: **RISC Microprocessor Architecture**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Customer No.: **40972**

**INFORMATION DISCLOSURE STATEMENT**

Pursuant to 37 C.F.R. §1.56, §1.97, and §1.98, Applicants bring the following documents to the Examiner's attention in the above-referenced application:

1. EP Patent Doc. EP 0 113 516, dated July 18, 1984 by INMOS Limited;
2. High Speed CMOS 4-bit Microcomputer SM-550 Series, FUKUI et al., SSD82-44, December 1982, Japanese document (pp 107-109) (English translation included);
3. The TMS34010: An Embedded Microprocessor, K. M. GUTTAG, et al., *IEEE Micro*, vol. 8, no. 3, June 1988, pp. 39-52;
4. The IBM System/ 360 Model 91: Machine Philosophy and Instruction-Handling, D.W.Anderson et al., *IBM Journal*, January 1967, pp. 8-24;
5. GE 625/635 Programming Reference Manual, *General Electric*, July 1964, Rev. January 1966;
6. IBM RT PC Romp Processor And Memory Management Unit Architecture, Simpson & Hester, *IBM Systems Journal*, Vol. 26, No. 4, 1987, pp. 346-360;
7. i860 64-bit Microprocessor: Advance Information, February 1989; and
8. MIPS: A Microprocessor Architecture, John Hennessy et al., *IEEE*, 1982, pp. 17-22.

A PTO form 1449 listing these documents is enclosed.

The relevance of the attached references is that were cited in other applications of Applicant related to similar technology.

An English abstract/translation is provided to satisfy the requirement for a concise explanation of the relevance of the non-English reference, as provided for in M.P.E.P. §609(III)(A)(3).

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

This information disclosure statement is filed after the mailing of an office action on the merits, but before the mailing of a final action. Therefore, pursuant to 37 CFR §1.97(c), the fee set forth in 37 CFR §1.17(p) is being paid by credit card with the electronic filing of this paper.

Respectfully Submitted,

August 8, 2008  
Date: \_\_\_\_\_

/Larry E. Henneman, Jr./

\_\_\_\_\_  
Larry E. Henneman, Jr., Reg. No. 41,063  
Henneman & Associates, PLC  
714 West Michigan Avenue  
Three Rivers, MI 49093

**CERTIFICATE OF ELECTRONIC FILING**

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being filed with the United States Patent and Trademark Office on the date shown below via the Office's Electronic Filing System.

Date: August 8, 2008

/Larry E. Henneman, Jr./

\_\_\_\_\_  
Larry E. Henneman, Jr.